

~~IN THE SPECIFICATION:~~

LC
7/06/06
insert above first paragraph on page 1

~~Please replace the cross-reference information for related Applications~~
~~beginning at the first line after the Title with the following updated information:~~

IDC-a1,AMD

✓ This Application is a continuation of Application Number
602
09/474,062 filed on 29 December 1999, now U.S. Patent Number 6,628,673.

Please replace the paragraph at page 1, lines 5-11 with the following
amended paragraph:

IDC-a2,AMD

✓ This application is related to the U.S. Patent Application 09/416,098 entitled "Method and Apparatus for Eliminating the Effects of Frequency Offsets in a Digital Communication System", filed on October 12, 1999 in the names of Teresa H. Meng, David K. Su and Masoud Zagari, also hereby incorporated by reference and referred to herein as "Meng I"; and to the U.S. Patent Application 09/422,829 entitled "Method and Apparatus for Closed-Loop and Open-Loop Timing Control for Digital Communications Systems" filed on October 21, 1999 in the name of Teresa H. Meng, hereby incorporated by reference and referred to herein as "Meng II".